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Docket No.: 122.1052CIPC2
Expedited Processing Requested

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Shigetoshi TOMIO, et al.

Serial No. 09/974,806

Group Art Unit: 2674

Confirmation No. 8860

Filed: October 12, 2001

Examiner: Duc Q. Dinh

For: FLAT DISPLAY PANEL HAVING INTERNAL POWER SUPPLY CIRCUIT FOR
REDUCING POWER CONSUMPTION

RESPONSE UNDER 37 CFR 1.116

RECEIVED

Assistant Commissioner for Patents
Washington, D.C. 20231

AUG 21 2003

Attention: Box AF

Technology Center 2600

Sir:

Applicants hereby respond to the FINAL Office Action mailed May 19, 2003 herein.

The Action sets forth only a single rejection, for obviousness-type double patenting of the pending claims 9-22 over claims 1-17 of USP 6,522,314. Claims 1-8, on the other hand, are allowable.

Enclosed herewith is a terminal disclaimer, executed by the undersigned counsel of record, for overcoming the rejections.

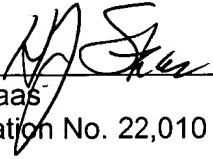
There being no other objections or rejections, it is submitted that the application is in condition for allowance, which action is earnestly solicited.

If any further fees are required in connection with this Response, please charge same to
Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

Date: August 19, 2003

By: 
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